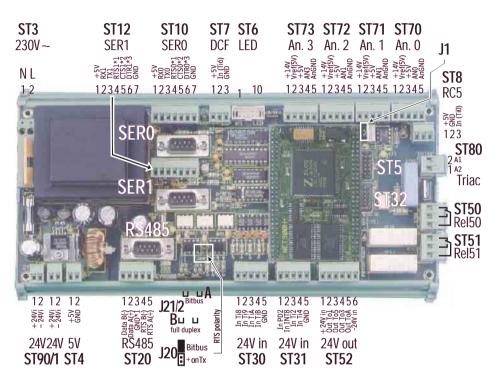
# 

# PER-EVA900

# NET/900H(+) Evaluation Board





### Mount NET/900H /900H +

With power off, insert the processor module with pins 1 showing towards the analog inputs ST70..73.

Re-check rapport of Pin 1 module to Pin 1 EVA900. Misplacing the module will

destroy the processor module and EVA900 circuitry!

# 24V i/o supply

The transformer on EVA900 provides 200mA @ 24V for external initiator/switch (inputs) and valve/relay (outputs) supply. The 24V outputs and the internal relays also need 24V to function. Please wire 24V from ST90 or 91 to ST52, observing polarity. You might substitute the internal supply with a stronger external supply wired directly to ST52 - 1(+)and 6(-).

# 5V supply

is available for small load (250mA max.) external logic on ST4.

### Programmer 's port

Please connect a 9p RS232 crossover (null modem) cable from SERO to your PC 's COM-port for mCAT download and monitor functions. ST10 is available in parallel for hardwired cabinet mount connection using screw terminals. Connect TX with the PC 's RX input and vice versa.

### Mains supply

Apply 230V ~ mains voltage to the terminals of ST3. Fix mains cable with a cable tie close to the board. Disconnect from mains when checking the fuse!

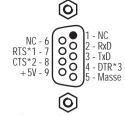
If you have 115V supply, you may (after disconnecting from mains) cut BR1 on the solder side of the board below the transformer and then connect BR2 and BR3. Then please mark the transformer top with "115V~".



# RS232 connectors

Both RS232 connectors are available as D9 and screw terminal ports. See the overview for screw terminal assignments.

ST10. 11: SER0 ST12. 13: SER1 \*1) RTS0 (IC3, bit7) \*1) RTS1 (SCLK1) \*2) CTSO (10K pull-up) \*2) CTS1 (P97/To7 + PU) \*3) DTRO (fixed high) \*3) DTR1 (fixed high)



If you need synchronous SPI-like mode on SER1 you might pull IC11 from its socket and access the raw processor pins at the socket as follows: 10 GND, 12 SCLK1, 13 TO7, 14 TxD1 and 15 RxD1. Use short wires only and with care applying excessive levels of voltage will destroy the processor module.

# BITBUS/RS485

LED-port: ST6

The RS485 is taken out to both a screw terminal connector (ST20) and the standard D9 connector. With other half duplex protocols than BITBUS, RTS polarity can be inverted. When full duplex RS485 is needed, the RTS wire pair can be switched setting J21/J22 to B to be the receive port. The data pair then becomes the transmitter only.

# ST21 (Plug):

0 1 - NC 2 - NC 3 - Data B (-) 4 - RTS B (-) 5 - Signal grou (Iso GND)\* NC - 7 Data Á (+) - 8 RTS A (+) - 9 \*) Normally pin 5 0 would be used

Signal ground

ST6

This connector mates the 4-digit 7-PO/CLKO - 10 9 - /LEDCS2 LEDDAT - 8 7 - LEDCLK Segment displays 7SEG4. LEDČLK and LEDDAT are clock and serial data lines. /DC0 - 6 5 - /CTS /LEDCS1 - 4 3 - /LEDCS0 all others except power are select lines +5V - 2 1 - GND to switch between displays. Set solder ioints on 7SEG4 to correspond to BR1 Select LEDCS"0" (default), "1" or "2" - all at the area on 7SEG4 CS 210 same "bus". See driver sample.

# 24V digital in: ST30/31

These are 8 optoisolated inputs to be supplied by external 24V. Two of them are supported as counters in mCAT.

Analog in: ST70 to 73

The analog inputs allow to supply

external sensor conditioning circuitry

5V digital supply, 5V reference. Input

ADCs. Conversion time is 15µs.

Caution! Analog inputs are raw

destroys the NET/900 module!

processor lines. Excessive voltage

with a variety of voltages: 14V raw DC,

range is 0..5V to NET/900H(+)'s 10-bit-

# 24V digital out: ST52

These 4 isolated high-side drivers switch small industrial valves or relays (up to 500mA/each, 0.8A total). Needs 24V!

# DCF77: ST7

Originally intended to read out time standard signals, this low-active input to TI6 (P96) is now open to any application.

# Triac: ST80 (J1)

Switches 230V ~ /1A. J1 must be set to enable the zero crossing detector to trigger NMI. Due to the high priority of this signal, J1 must not be set during flash EPROM programming.

# PG-Port: ST5

The raw processor pins (caution!) of the pulse generator (stepper motor) port can be connected to external drivers. Use short cables only! No standard software support due to the diversity of motors.

> PG13 - 10 9 - PG12 PG11 - 8 7 - PG10 PG03 - 6 5 - PG02 PG01 - 4 3 - PG00 + 5V - 2 1 - GND

# TMux-Inputs: ST32

The pins on this port are 3 inputs to the Ti8 timer that are multiplexed by setting bits D5 and D6 of HCT273 IC3 (Addr.

+5V - 10 9 - GND TMux2 - 8 7 - GND TMux1 - 6 5 - GND TMux0 - 4 3 - GND +5V - 2 1 - GND

# Relays: ST50/51

The 2 changeover relays optoisolated from T09/PB3 and T08/PB2 are capable to switch up to mains voltage (250V-/8A). Needs 24V on ST52!

# RC5: ST8

This input to TiO is used to decode RC5 infrared remote controls. Use the SFH506-36 (obsolete) infrared receiver as input device. When using the new SFH5110-36 instead, please change R6 to 100 Ohms.

Note: Using EVA900 with NET/900H + obscures part of the LED's of the 24V-i/o!



When used with NET/900H or NET/900H + , these can be equipped with an mCAT that supports the following i/o of EVA/900 with Express-I/O: 24V inputs (Ti9, TiB as HDMA counters), input Ti6 (DCF77), 24V outputs (To3/To5 PWM), relays, analog inputs, TRIAC (just switched), RC5 receiver. **Not supported** are the stepper port, TMux and LED port. There is, however, a sample task for the 7SEG4 display on the LCD port. Both require access to IC3, HCT273 that can't be read back. So there's need for write coordination! Ti6.7.8 and 9 are free, Ti1 and 5 with possible conflicts (Ti0.4)